

# 2N7002H-HF

**RoHS Device  
Halogen Free  
N-Channel**



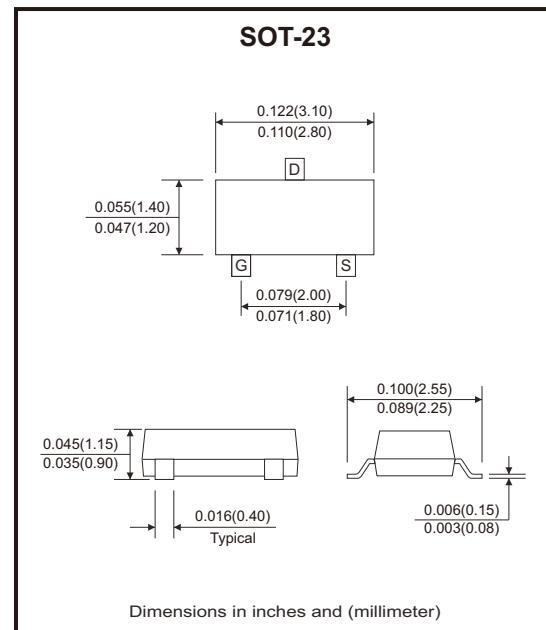
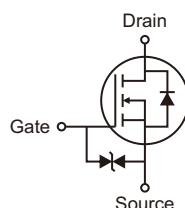
## Features

- N-Channel enhancement mode field effect transistor, Designed for high speed pulse amplifier and drive application, which is manufactured by the N-Channel Dmos process.
- ESD MIL-STD 833, ±2.5KV contact discharge compliant protection.

## Mechanical data

- Case: SOT-23, molded plastic.
- Solderability: MIL-STD-202, Method 208.

## Circuit Diagram



## Maximum Rating (at TA=25°C unless otherwise noted) (Note 2)

| Parameter  | Symbol           | Value       | Unit |
|--|------------------|-------------|------|
| Drain-source voltage                             | V <sub>DSS</sub> | 60          | V    |
| Gate-source voltage                              | V <sub>GSS</sub> | ±20         | V    |
| Drain current (pulsed) (Note 3)                  | I <sub>DM</sub>  | 800         | mA   |
| Drain current (continuous)                       | I <sub>D</sub>   | 300         |      |
| Maximum power dissipation                        | P <sub>D</sub>   | 350         | mW   |
| Operating junction temperature range             | T <sub>J</sub>   | -55 to +150 | °C   |
| Storage temperature range                        | T <sub>STG</sub> | -55 to +150 | °C   |
| Thermal resistance, junction to ambient (Note 1) | R <sub>θJA</sub> | 357         | °C/W |
| Thermal resistance, junction to case (Note 1)    | R <sub>θJC</sub> | 90          | °C/W |

Notes: 1. R<sub>θJA</sub> is the sum of junction to case and case to ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R<sub>θJC</sub> is guaranteed by design while R<sub>θCA</sub> is determined by the user's board design. The value of R<sub>θJA</sub> is measured with device mounted on 1 in<sup>2</sup> FR-4 board with 2 oz copper.

- Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
- Pulse width limited by maximum junction temperature.

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**Electrical Characteristics** (at  $T_c=25^\circ\text{C}$  unless otherwise noted)

| Parameter   | Symbol                     | Conditions  | Min | Typ  | Max      | Unit          |
|---|----------------------------|---|-----|------|----------|---------------|
| <b>Off Characteristics</b>                                    |                            |   |     |      |          |               |
| Drain-source breakdown voltage                                | $\text{BV}_{\text{DSS}}$   | $V_{\text{GS}}=0\text{V}, I_{\text{D}}=10\mu\text{A}$   | 60  |      |          | V             |
| Drain-source leakage current                                  | $I_{\text{DS}(\text{SS})}$ | $V_{\text{GS}}=0\text{V}, V_{\text{DS}}=60\text{V}, T_J=25^\circ\text{C}$                       |     |      | 1        | $\mu\text{A}$ |
| Gate-source leakage current                                   | $I_{\text{GSS}}$           | $V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 20\text{V}$   |     |      | $\pm 10$ | $\mu\text{A}$ |
| <b>On Characteristics (Note 1)</b>                            |                            |   |     |      |          |               |
| Gate threshold voltage  | $V_{\text{GS}(\text{th})}$ | $V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$                                      | 1.0 | 1.7  | 2.5      | V             |
| Static drain-source on resistance                             | $R_{\text{DS}(\text{ON})}$ | $V_{\text{GS}}=10\text{V}, I_{\text{D}}=300\text{mA}$   |     | 2.0  | 3.0      | $\Omega$      |
| <b>Dynamic Characteristics</b>                                |                            |   |     |      |          |               |
| Input capacitance   | $C_{\text{iss}}$           | $V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$                              |     |      | 50       | pF            |
| Output capacitance  | $C_{\text{oss}}$           |   |     |      | 25       |               |
| Reverse transfer capacitance                                  | $C_{\text{rss}}$           |   |     |      | 5        |               |
| <b>Switching Characteristics</b>                              |                            |   |     |      |          |               |
| Turn-on delay time  | $t_{\text{d(on)}}$         | $V_{\text{DD}}=30\text{V}, I_{\text{D}}=200\text{mA}, R_L=150\Omega, V_{\text{GEN}}=10\text{V}$ |     | 6    |          | nS            |
| Turn-on rise time   | $t_r$                      |   |     | 5    |          |               |
| Turn-off delay time   | $t_{\text{d(off)}}$        |   |     | 25   |          |               |
| Turn-off fall time  | $t_f$                      |   |     | 15   |          |               |
| <b>Drain-Source Diode Characteristics and Maximum Ratings</b> |                            |   |     |      |          |               |
| Drain-source diode forward voltage                            | $V_{\text{SD}}$            | $V_{\text{GS}}=0\text{V}, I_{\text{D}}=300\text{mA}$  |     | 0.85 | 1.5      | V             |

Notes: 1. Pulse test : Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$

**Rating and Characteristic Curves (2N7002H-HF)**

Fig.1 - Output Characteristics

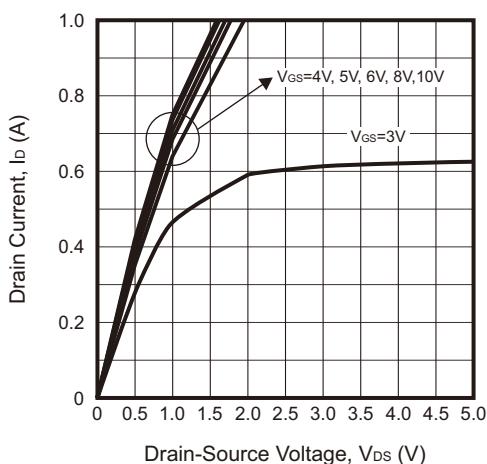
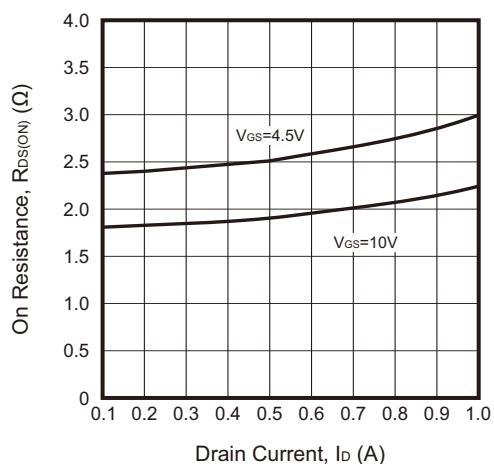


Fig.2 - Drain-Source On Resistance



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## Rating and Characteristic Curves (2N7002H-HF)

Fig.3 - Transfer Characteristics

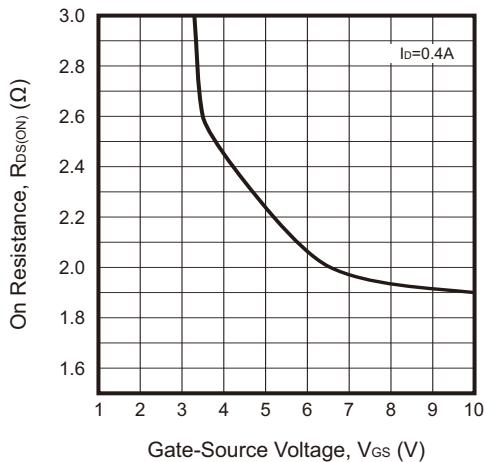


Fig.4 - Gate Threshold Voltage

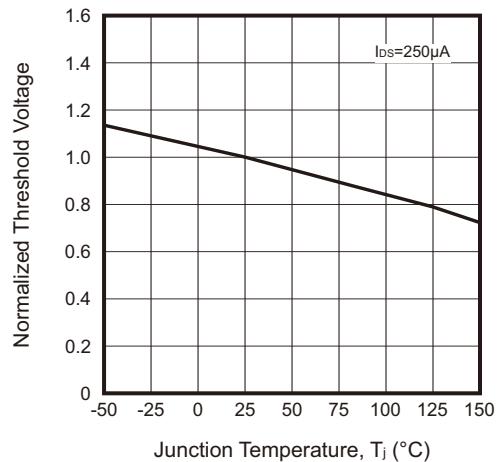


Fig.5 - Drain-Source On Resistance

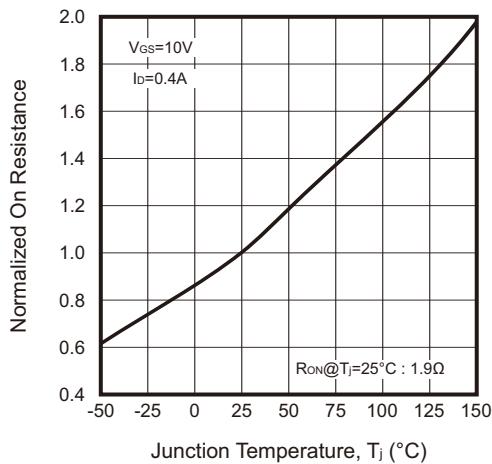


Fig.6 - Source-Drain Diode Forward

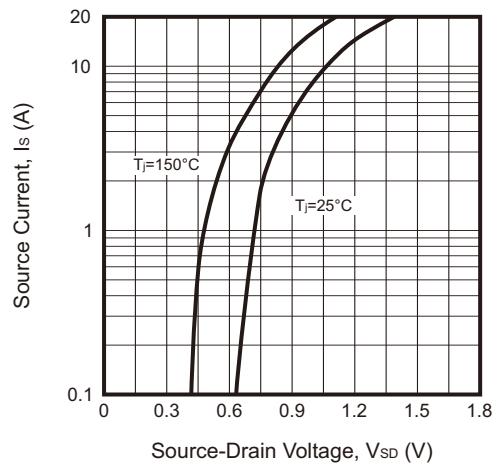


Fig.7 - Capacitance

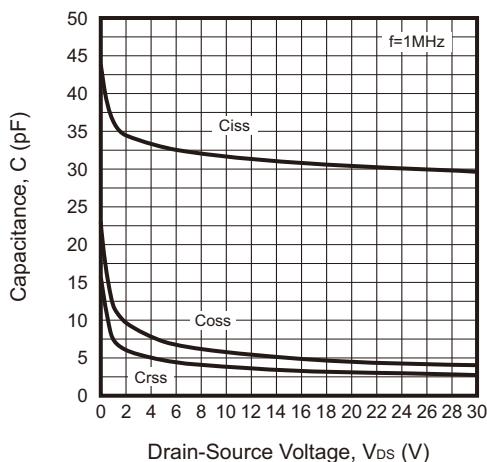
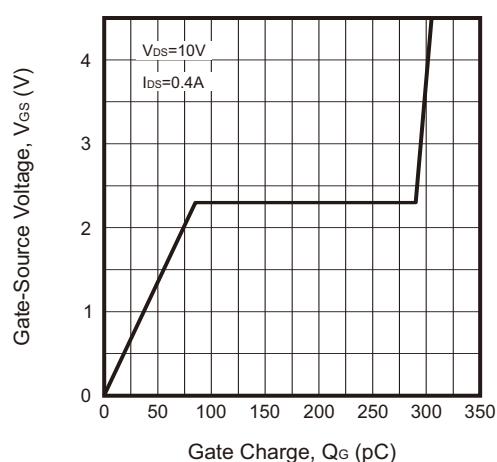
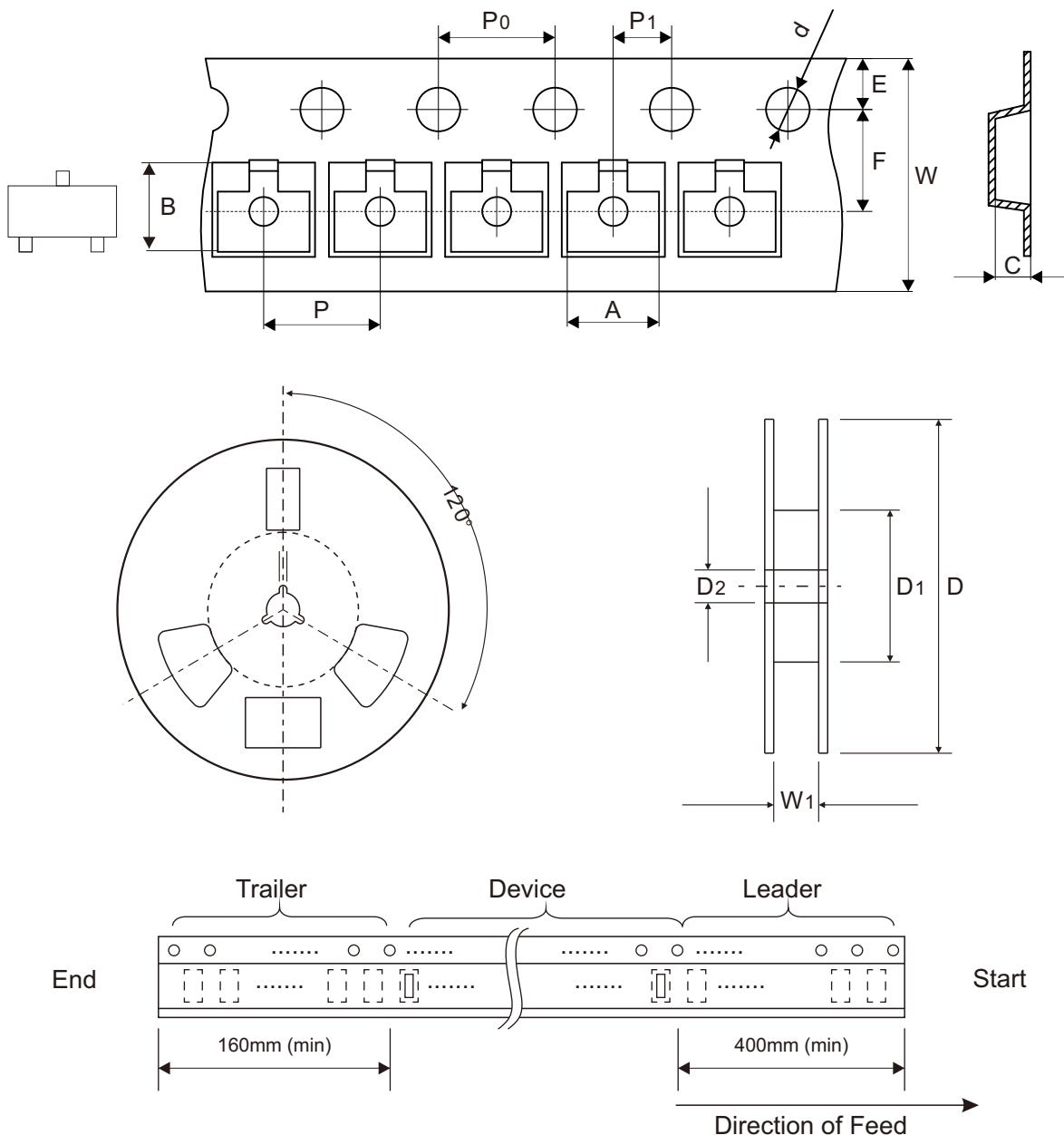


Fig.8 - Gate Charge



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## Reel Taping Specification



|        | SYMBOL | A                 | B                 | C                 | d                 | D                 | D <sub>1</sub>    | D <sub>2</sub>    |
|--------|--------|-------------------|-------------------|-------------------|-------------------|-------------------|-------------------|-------------------|
| SOT-23 | (mm)   | $3.15 \pm 0.10$   | $2.77 \pm 0.10$   | $1.22 \pm 0.10$   | $1.50 \pm 0.10$   | $178.00 \pm 1.00$ | $54.00 \pm 0.50$  | $13.00 \pm 0.50$  |
|        | (inch) | $0.124 \pm 0.004$ | $0.109 \pm 0.004$ | $0.048 \pm 0.004$ | $0.059 \pm 0.004$ | $7.008 \pm 0.039$ | $2.126 \pm 0.020$ | $0.512 \pm 0.020$ |

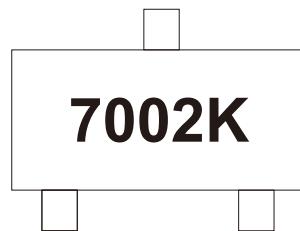
|        | SYMBOL | E                 | F                 | P                 | P <sub>0</sub>    | P <sub>1</sub>    | W                             | W <sub>1</sub>    |
|--------|--------|-------------------|-------------------|-------------------|-------------------|-------------------|-------------------------------|-------------------|
| SOT-23 | (mm)   | $1.75 \pm 0.10$   | $3.50 \pm 0.05$   | $4.00 \pm 0.10$   | $4.00 \pm 0.10$   | $2.00 \pm 0.05$   | $8.00 \pm 0.30$<br>$-0.10$    | $9.50 \pm 1.00$   |
|        | (inch) | $0.069 \pm 0.004$ | $0.138 \pm 0.002$ | $0.157 \pm 0.004$ | $0.157 \pm 0.004$ | $0.079 \pm 0.002$ | $0.315 \pm 0.012$<br>$-0.004$ | $0.374 \pm 0.039$ |

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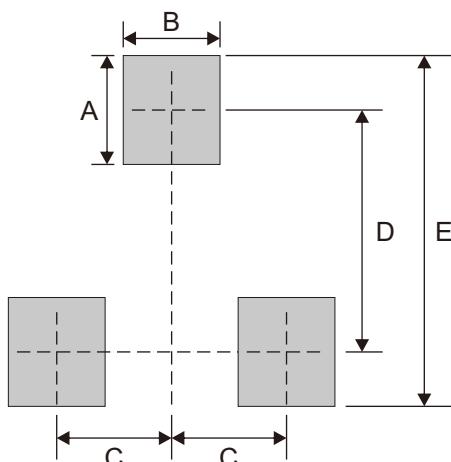
## Marking Code

| Part Number | Marking Code |
|-------------|--------------|
| 2N7002H-HF  | 7002K        |



## Suggested PAD Layout

| SIZE | SOT-23 |        |
|------|--------|--------|
|      | (mm)   | (inch) |
| A    | 0.90   | 0.035  |
| B    | 0.80   | 0.031  |
| C    | 0.95   | 0.037  |
| D    | 2.00   | 0.079  |
| E    | 2.90   | 0.114  |



## Standard Packaging

| Case Type | REEL PACK     |                     |
|-----------|---------------|---------------------|
|           | REEL<br>(pcs) | Reel Size<br>(inch) |
| SOT-23    | 3,000         | 7                   |

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