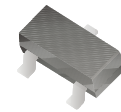


CMSP1013V3-HF

P-Channel

RoHS Device

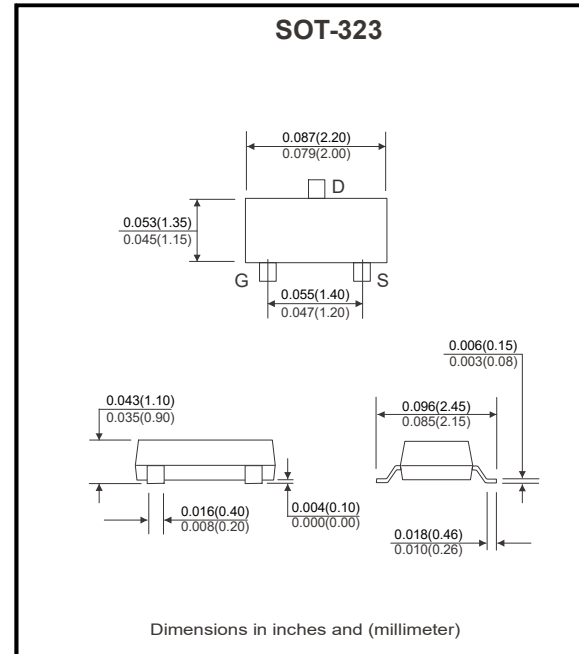
Halogen Free



$V_{(BR)DSS}$	$R_{DS(on)}$ Typ	I_D
-20V	0.64Ω @ -4.5V	-540mA
	1.1Ω @ -2.5V	
	1.9Ω @ -1.8V	

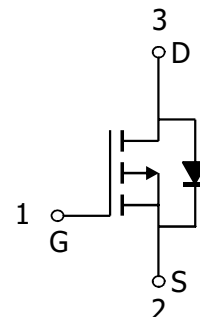
Features

- Very low level gate drive requirements allowing direct operation in 3V circuits. $V_{GS(th)} < 1.2V$.
- Compact industrial standard SOT-323 surface mount package.
- ESD protected gate
- Pb-free lead plating and halogen-free package.



Circuit diagram

- 1 G : Gate
2 S : Source
3 D : Drain



Maximum Ratings (at $T_a = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 10	
Continuous Drain Current @ $T_A = 25^\circ C$, $V_{GS} = 4.5V$	I_D	-0.54	A
Pulsed Drain Current (Note 1)	I_{DM}	-1.5	
Maximum Power Dissipation @ $T_A = 25^\circ C$ (Note 2)	P_D	350	mW
Thermal Resistance, Junction-to-Ambient	$R_{th,ja}$	357	$^\circ C/W$
Operating Junction and Storage Temperature	T_j, T_{stg}	-55~+150	$^\circ C$

Note : 1. Pulse width $\leq 10 \mu s$, duty cycle $\leq 2\%$.

2. Surface mounted on 1 in² copper pad of FR-4 board, $t \leq 5s$.

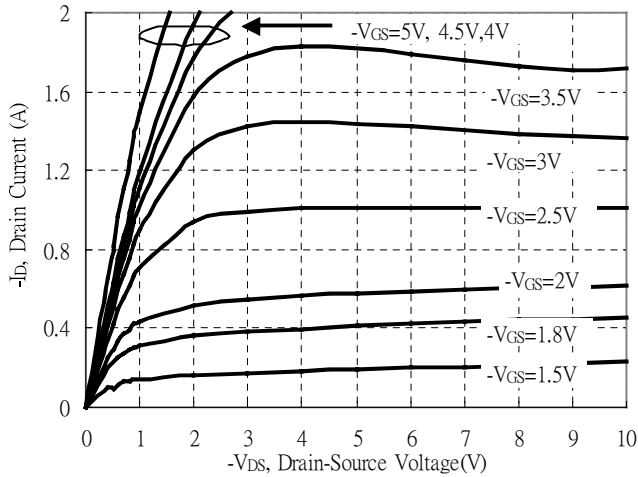
Electrical Characteristics (at $T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-20	-	-	V	V _{GS} =0V, I _D =-250μA
V _{GS(th)}	-0.5	-0.8	-1.2	V	V _{DS} =V _{GS} ,I _D =-250μA
G _{FS}	-	0.5	-	S	V _{DS} =-10V,I _b =-200mA
I _{GSS}	-	-	±10	μA	V _{GS} =±10V,V _{DS} =0V
I _{DSS}	-	-	-1		V _{DS} =-20V,V _{GS} =0V
	-	-	-10		V _{DS} =-20V,V _{GS} =0V, T _j =55℃
*R _{DS(ON)}	-	0.64	0.9	Ω	V _{GS} =-4.5V,I _b =-430mA
	-	0.68	0.9		V _{GS} =-4V,I _b =-300mA
	-	1.1	1.4		V _{GS} =-2.5V,I _b =-300mA
	-	1.9	2.7		V _{GS} =-1.8V,I _b =-150mA
Dynamic					
C _{iss}	-	59	-	pF	V _{DS} =-10V, V _{GS} =0V, f=1MHz
C _{oss}	-	21	-		
C _{rss}	-	15	-		
*t _{d(ON)}	-	5	-	ns	V _{DS} =-6V, I _D =-500mA, V _{GS} =-4.5V, R _G =50Ω
*t _r	-	6	-		
*t _{d(OFF)}	-	42	-		
*t _f	-	14	-		
*Q _g	-	1.2	-	nC	V _{DS} =-5V, I _D =-250mA, V _{GS} =-4.5V
*Q _{gs}	-	0.38	-		
*Q _{gd}	-	0.23	-		
Source-Drain Diode					
*I _S	-	-	-0.54	A	
*I _{SM}	-	-	-1.5		
*V _{SD}	-	-0.77	-1.2	V	V _{GS} =0V, I _s =-100mA

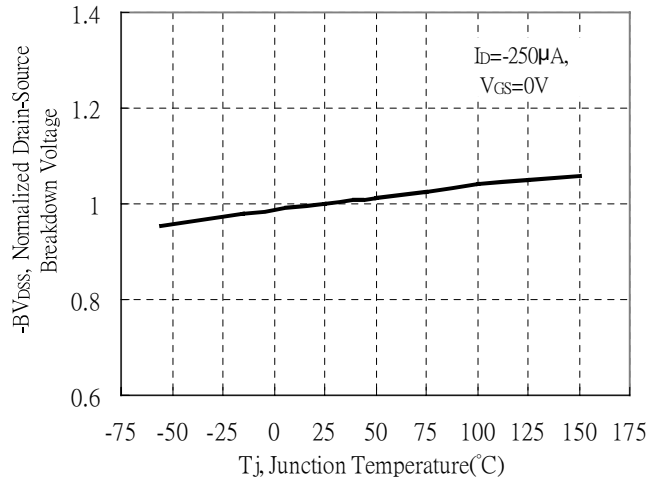
*Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.

Typical Characteristics

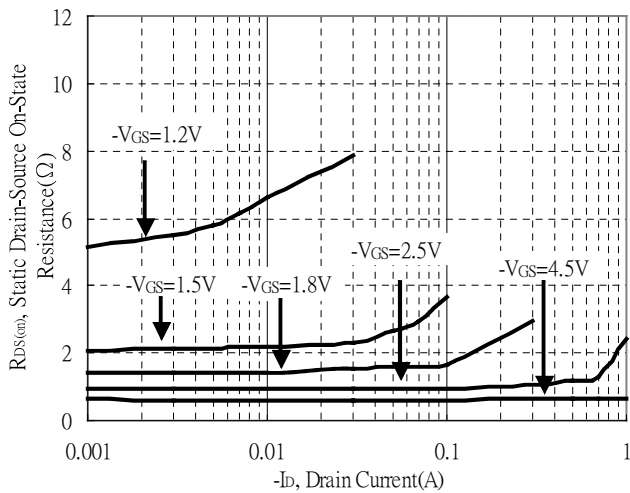
Typical Output Characteristics



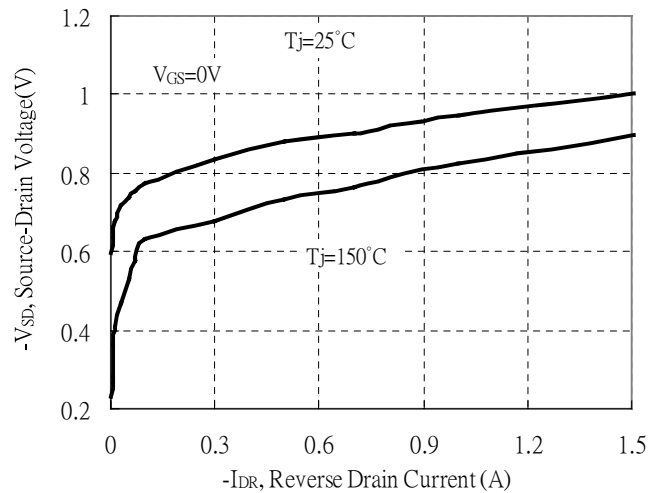
Breakdown Voltage vs Ambient Temperature



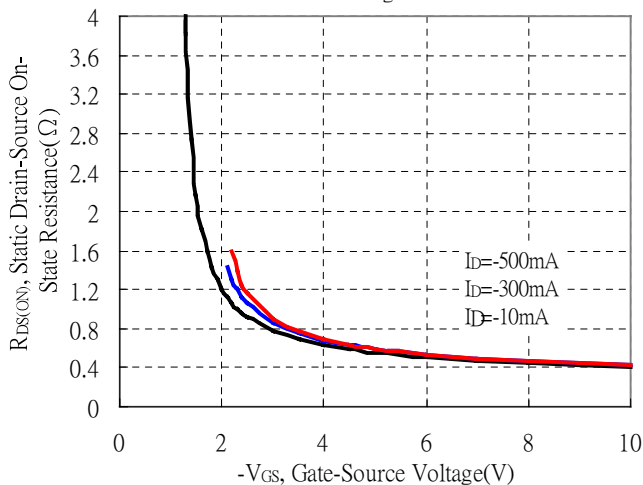
Static Drain-Source On-State resistance vs Drain Current



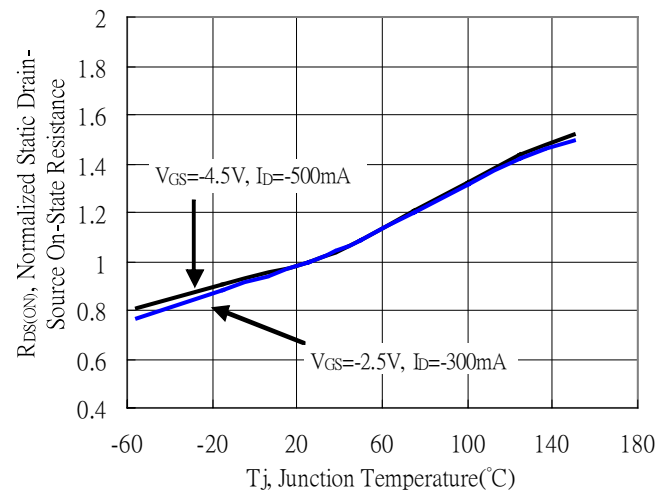
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

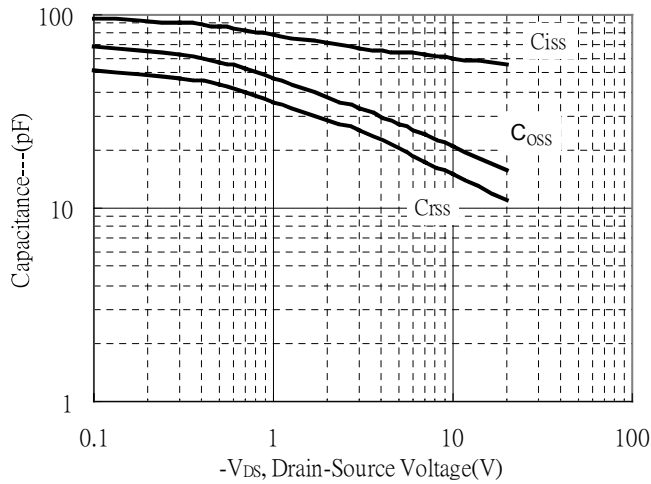


Drain-Source On-State Resistance vs Junction Temperature

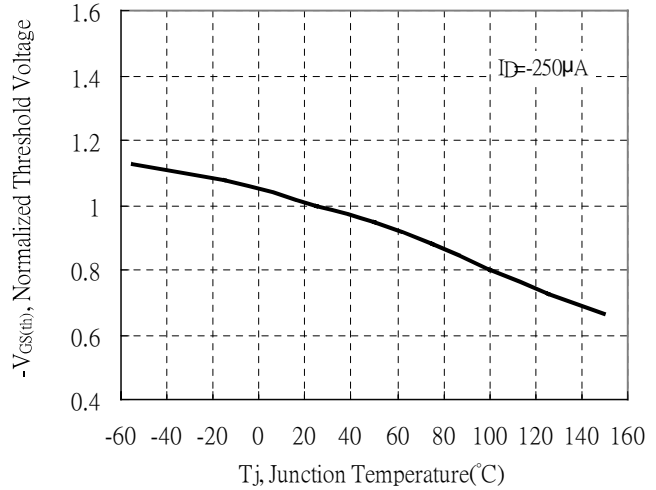


Typical Characteristics(Cont.)

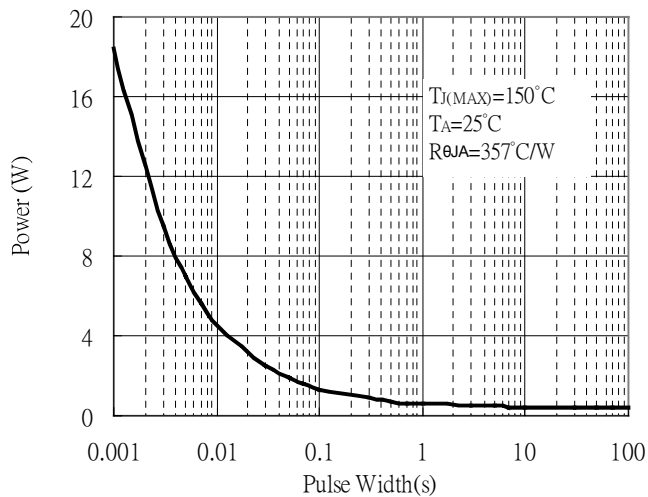
Capacitance vs Drain-to-Source Voltage



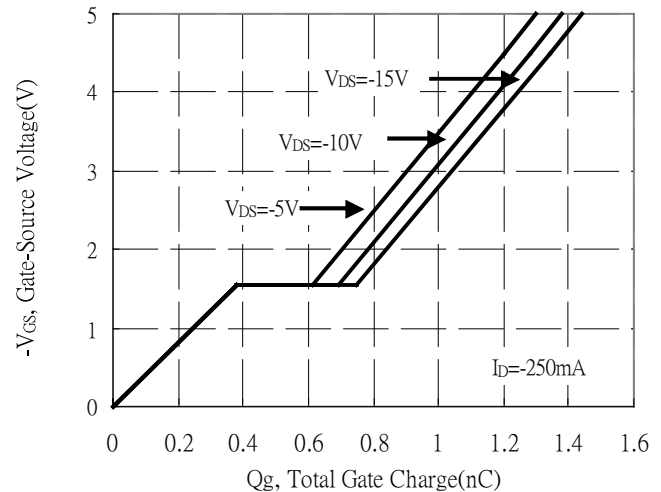
Threshold Voltage vs Junction Temperature



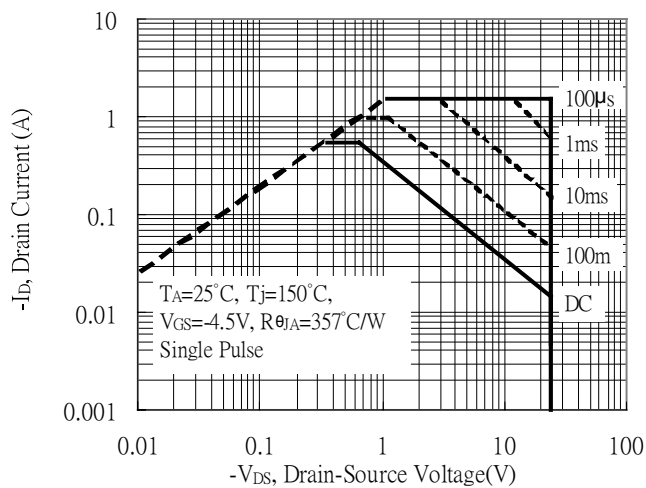
Single Pulse Power Rating, Junction to Ambient



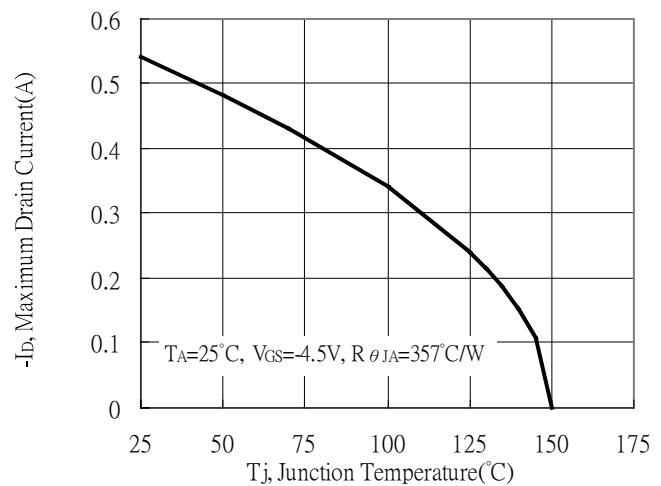
Gate Charge Characteristics



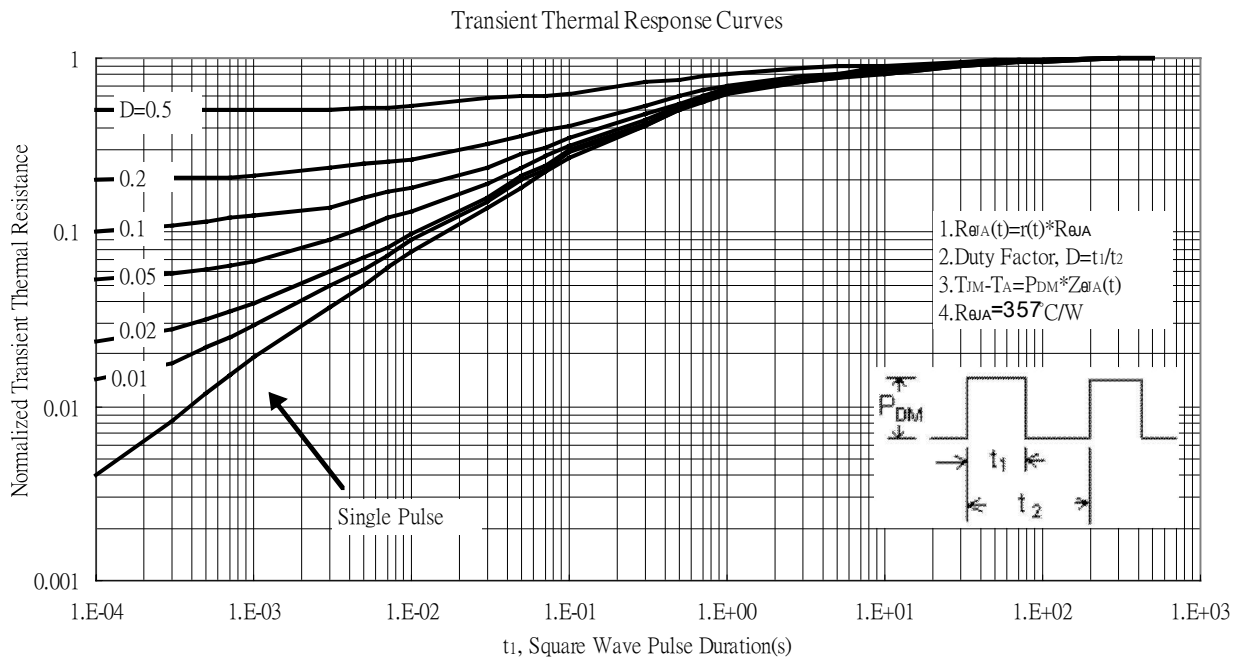
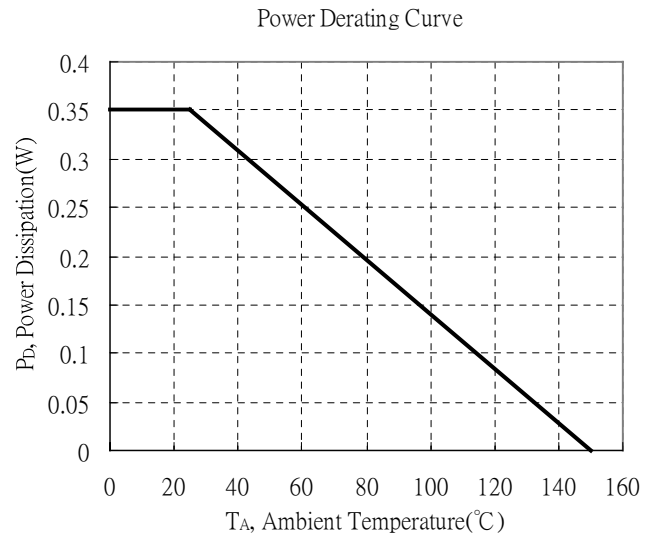
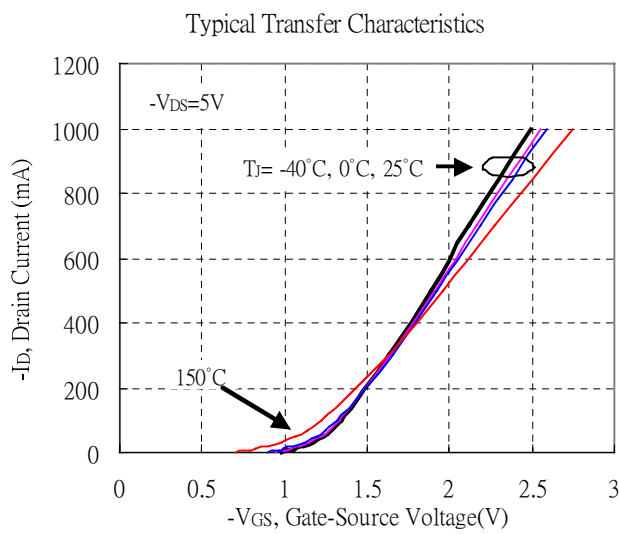
Maximum Safe Operating Area



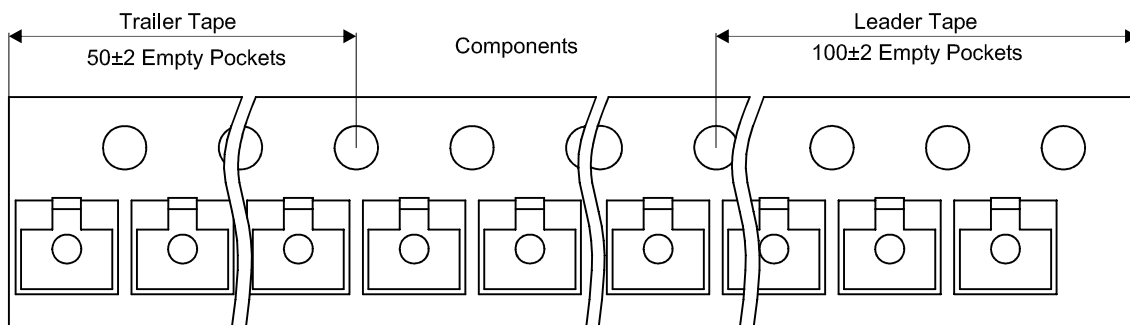
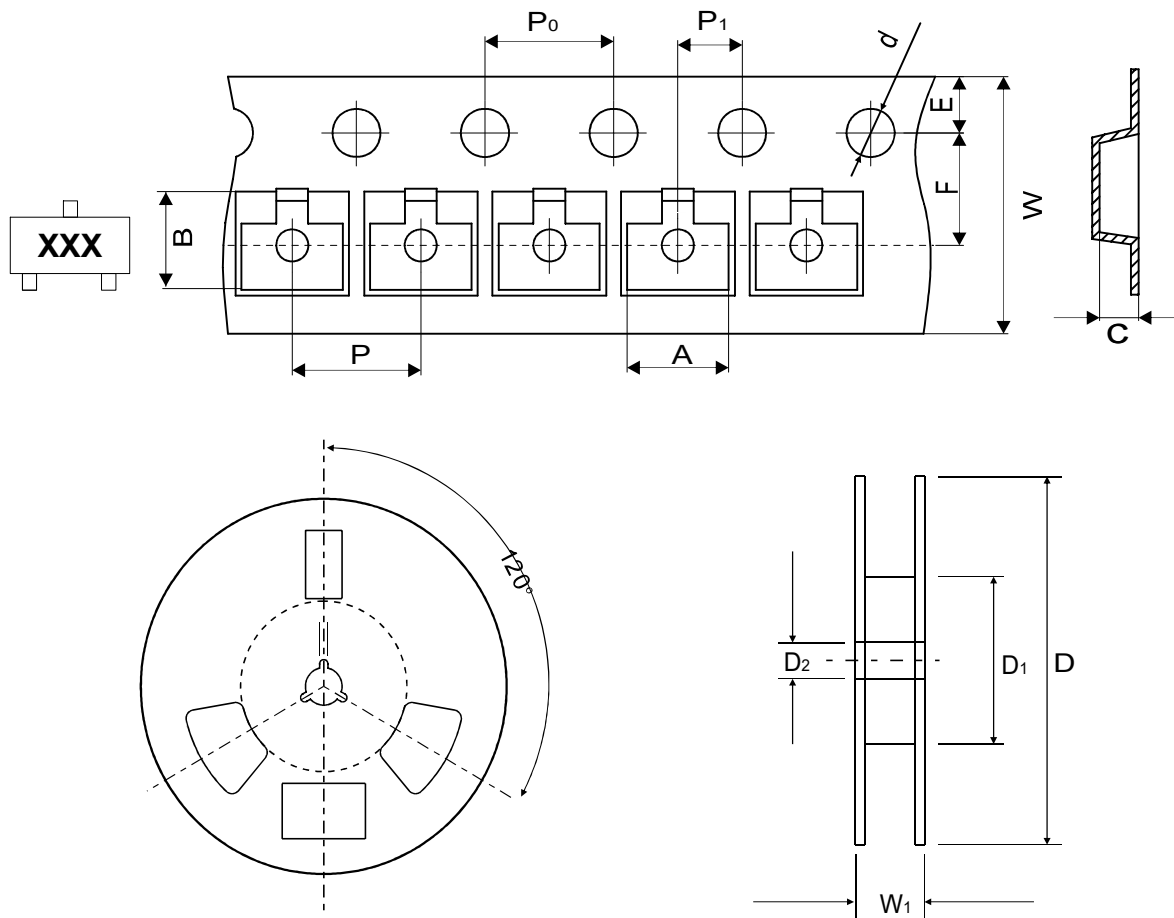
Maximum Drain Current vs Junction Temperature



Typical Characteristics(Cont.)



Reel Taping Specification



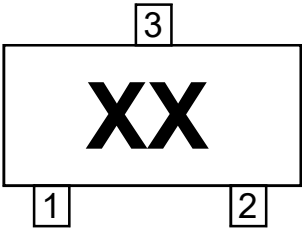
SOT-323	SYMBOL	A	B	C	d	D	D1	D2
	(mm)	2.25 ± 0.05	2.55 ± 0.05	1.19 ± 0.05	1.55 ± 0.10	178.00 ± 2.00	54.40 ± 1.00	13.00 ± 1.00
	(inch)	0.089 ± 0.002	0.100 ± 0.002	0.047 ± 0.002	0.061 ± 0.004	7.008 ± 0.079	2.142 ± 0.039	0.512 ± 0.039

SOT-323	SYMBOL	E	F	P	P0	P1	W	W1
	(mm)	1.75 ± 0.10	3.50 ± 0.10	4.00 ± 0.10	4.00 ± 0.10	2.00 ± 0.10	8.00 + 0.30/-0.10	12.30 ± 1.00
	(inch)	0.069 ± 0.004	0.138 ± 0.004	0.157 ± 0.004	0.157 ± 0.004	0.079 ± 0.004	0.315 + 0.012/-0.004	0.484 ± 0.039

Company reserves the right to improve product design , functions and reliability without notice.

Marking Code

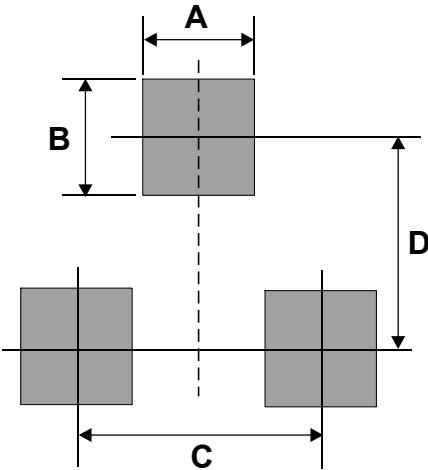
Part Number	Marking Code
CMSP1013V3-HF	TW



xx = Product type marking code

Suggested PAD Layout

SIZE	SOT-323	
	(mm)	(inch)
A	0.50	0.020
B	0.80	0.031
C	1.30	0.012
D	2.20	0.087



Standard Packaging

Case Type	REEL PACK	
	REEL (pcs)	Reel Size (inch)
SOT-323	3,000	7